

FEATURES:

- Up to 32K bytes of TMS 2716 EPROM.
- Up to 16K bytes of TMS 2114 Static RAM.
- 1 microsecond cycle time (3 MHz).
- Bus compatible with T-BUS TM990/10X CPU boards.
- TM990/201-44 compatible with TM990/1481.
- 16 memory maps for both EPROM and RAM.
- Selectable Wait State generator.
- 0–70°C temperature range.
- Factory burnt-in.

DESCRIPTION:

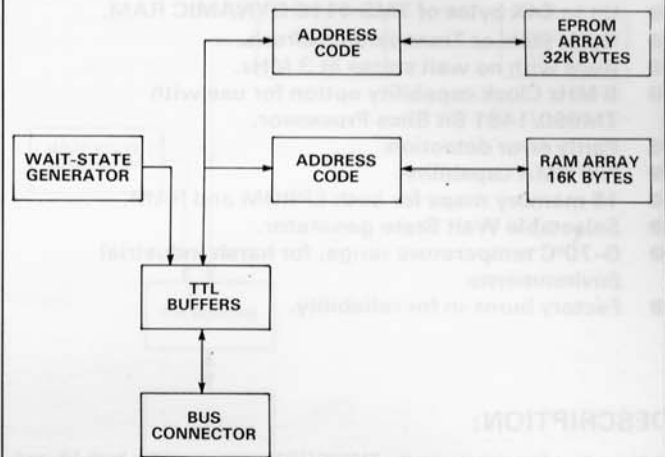
The TM990/201 memory expansion board is a member of Texas Instruments line of OEM computer products which takes advantage of Texas Instruments broad based semiconductor technology to provide economical, computer based solutions for OEM applications. The memory expansion board is contained on a 7½ × 11 inch printed circuit board which is fully compatible with the TM990 board format.

The TM990/201 features up to 16K bytes of static RAM and up to 32K bytes of EPROM. The static RAM array is composed of Texas Instruments TMS 4014/2114 1K × 4 static memory devices. The EPROM array comprises Texas Instruments TMS 2716, 2K × 8 EPROM devices. The static RAM array is arranged into four banks of memory, each 2K × 16. The EPROM array is likewise arranged into eight banks, each 2K × 16. Both memory arrays are socketed for convenient memory expansion. (The TM990/201-42 and TM990/201-43 are fully socketed).

The TM990/201 memory controller logic provides the timing and memory mapping functions necessary to interface the TM990/201 to 16-bit TM990/10X series microcomputers. The memory map is switch selectable for both the RAM and EPROM arrays. Sixteen convenient memory map configurations are possible for each array, and the maps are configured on 4K byte address boundaries. The map logic also is designed to accommodate customized memory maps.

The TM990/201-4X family of memory expansion boards is populated with TMS 4014/2114-45 static RAM's and TMS 2176 EPROM's. Both devices offer 450 nsec access time; consequently, each memory cycle to the TM990/201 is extended one clock cycle by the insertion of a wait state. If faster static RAM's are utilized in the RAM array, the WAIT state in RAM memory cycles can be conveniently removed using only a jumper.

BLOCK DIAGRAM:



RAM DECODE CONFIGURATIONS:

AD A3 HEX	HEX MEMORY ADDRESS	MICROCOMPUTER MEMORY MAP	SWITCH CODES*															
			0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
0	0000 0FFF	EPROM	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	
1	1000 1FFF	EPROM (EXPAN)	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	
2	2000 2FFF		ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	
3	3000 3FFF		ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	
4	4000 4FFF		ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	
5	5000 5FFF		ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	
6	6000 6FFF		ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	
7	7000 7FFF		ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	
8	8000 8FFF		ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	
9	9000 9FFF		ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	
A	A000 AFFF		ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	
B	B000 BFFF		ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	
C	C000 CFFF		ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	
D	D000 DFFF		ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	
E	E000 EFFF	MAPPED I/O	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	
F	F000 FFFF	RAM	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	

N.B. THE TM990/201-44 HAS DIFFERENT MEMORY CODE CONFIGURATIONS

EPROM DECODE CONFIGURATIONS:

AD A3 HEX	HEX MEMORY ADDRESS	MICROCOMPUTER MEMORY MAP	SWITCH CODES*															
			0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
0	0000 0FFF	EPROM	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	
1	1000 1FFF	EPROM (EXPAN)	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	
2	2000 2FFF		ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	
3	3000 3FFF		ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	
4	4000 4FFF		ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	
5	5000 5FFF		ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	
6	6000 6FFF		ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	
7	7000 7FFF		ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	
8	8000 8FFF		ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	
9	9000 9FFF		ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	
A	A000 AFFF		ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	
B	B000 BFFF		ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	
C	C000 CFFF		ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	
D	D000 DFFF		ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	
E	E000 EFFF	MAPPED I/O	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	
F	F000 FFFF	RAM	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	

N.B. THE TM990/201-44 HAS DIFFERENT MEMORY CODE CONFIGURATIONS

ORDERING INFORMATION:

Part number	EPROM	RAM	Typical Power Requirements		
			+5V	+12V	-12V
TM990/ 201-41	8K (16K)	4K (8K)	1.0 A	160 mA	50 mA
TM990/ 201-42	16K (32K)	8K (16K)	1.4 A	225 mA	125 mA
TM990/ 201-43	32K (32K)	16K (32K)	2.2 A	475 mA	225 mA
TM990/ 201-44	0 (32K)	16K @ 5 MHz	2.2 A	475 mA	225 mA

Numbers in brackets refer to area socketed. All memory sizes are in bytes.

RAM MEMORY MAP

			SWITCH NO.	SWITCH CODES*															
				0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
A0-A3 (HEX)	HEX MEMORY ADDRESS	MICROCOMPUTER MEMORY MAP	5	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF
			6	ON	ON	OFF	OFF	ON	ON	OFF	OFF	ON	ON	OFF	OFF	ON	ON	OFF	OFF
		/100 AND /101	7	ON	ON	ON	ON	OFF	OFF	OFF	OFF	ON	ON	ON	ON	OFF	OFF	OFF	OFF
			8	ON	ON	ON	ON	ON	ON	ON	ON	ON	OFF	OFF	OFF	OFF	OFF	OFF	OFF
0	0000-0FFF	EPROM									RBLK0								
1	1000-1FFF	EPROM (EXP)																	
2	2000-2FFF										RBLK2								
3	3000-3FFF																		
4	4000-4FFF																		
5	5000-5FFF																		
6	6000-6FFF																		
7	7000-7FFF																		
8	8000-8FFF																		
9	9000-9FFF																		
A	A000-AFFF																		RBLK0
B	B000-BFFF																		
C	C000-CFFF																		
D	D000-DFFF																		
E	E000-EFFF	MAPPED I/O																	
F	F000-FFFF	RAM																	

RAM DISABLED

* OFF = 1. ON = 0.

↑ RAM block numbers marked on PCB.

EPROM MEMORY MAP

			SWITCH NO.	SWITCH CODES*																	
				0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F		
A0-A3 (HEX)	HEX MEMORY ADDRESS	MICROCOMPUTER MEMORY MAP		1	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	ON	OFF	
				2	ON	ON	OFF	OFF	ON	ON	OFF	OFF	ON	ON	OFF	OFF	ON	ON	OFF	OFF	ON
		/100 AND /101		3	ON	ON	ON	ON	OFF	OFF	OFF	OFF	ON	ON	ON	ON	OFF	OFF	OFF	OFF	OFF
				4	ON	ON	ON	ON	ON	ON	ON	ON	ON	OFF	OFF	OFF	OFF	OFF	OFF	OFF	OFF
0	0000-0FFF	EPROM		16K WORDS (16 × 2716s)																	
1	1000-1FFF	EPROM (EXP)																			
2	2000-2FFF					EBLK7					8K WORDS (8 × 2716s)					4K WORDS (4 × 2716s)					
3	3000-3FFF											EBLK7						EBLK7			
4	4000-4FFF																		EBLK6		
5	5000-5FFF											EBLK4									
6	6000-6FFF																				
7	7000-7FFF																				
8	8000-8FFF																				
9	9000-9FFF																				
A	A000-AFFF																				
B	B000-BFFF																				
C	C000-CFFF																				
D	D000-DFFF																				
E	E000-EFFF	MAPPED I/O																			
F	F000-FFFF	RAM																			

EPROM DISABLED

* OFF = 1. ON = 0.

↑ EPROM block numbers marked on PCB.